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- an ATM core switch for outputting cells inputted thereto from said input side circuit interface or interfaces to said output side circuit interface or interfaces;

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on the feedback.

3. An ATM switch as claimed in claim 2, wherein each of said output side circuit interfaces includes an output virtual channel cell rate control section for storing a cell number  
5 accumulated for each virtual channel, an output virtual path cell rate control section for controlling the cell rate for each virtual channel based on the cell number accumulated in said output virtual channel cell rate control section, and a physical layer section for outputting a cell from said output  
10 virtual channel cell rate control section to a circuit, said output virtual channel cell rate control section feeding back the cell number to said input virtual channel cell rate control section.

4. An ATM switch as claimed in claim 1, wherein said ATM  
15 core switch includes multiplexing means for multiplexing cells from all of said output side circuit interface sections, filter means for comparing output port identification numbers applied to the cells with output port numbers of said filter means themselves and passing therethrough only those cells which  
20 exhibit coincidence in the comparison, and a cell buffer of the first-in first-out type provided for each output port for temporarily storing those cells which have passed through the corresponding filter means, converting the rate of the cells and outputting the resulting cells to a corresponding one of  
25 said output side circuit interfaces.

5. An ATM switch as claimed in claim 2, wherein said input

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cell rate control section stores an input circuit number, a service class, a minimum cell rate, an output switch port number and an intra-switch connection identification number of contents of a contract concluded in advance in a corresponding relationship to a virtual path identifier/virtual channel identifier of an input cell.

6. An ATM switch as claimed in claim 3, wherein said output virtual channel cell rate control section stores a service class, a virtual channel minimum cell rate, a virtual channel peak cell rate, a virtual path peak cell rate, an output circuit number and an output virtual path identifier/virtual channel identifier of contents of a contract concluded in advance in a corresponding relationship to an intra-switch connection identification number of each cell.